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#42825

PTO/SB/21 (6-99)

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TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

Application Number

10/071,862

Filing Date

February 7, 2002

First Named Inventor

Limin He

Group/Art Unit

2825

Examiner Name

Unassigned

Total Number of Pages in This Submission

4

Attorney Docket Number

24224-702

ENCLOSURES (check all that apply)

☐ Fee Transmittal Form

☐ Fee Attached

☒ Preliminary Amendment

☐ After Final

☒ Version with Markings Showing Changes

☐ Affidavits/declaration(s)

☐ Extension of Time Request

☐ Information Disclosure Statement

☐ Certified Copy of Priority Document(s)

☐ Response to Missing Parts/Incomplete Application

☐ Response to Missing Parts under 37 CFR 1.52 or 1.53

☐ Assignment Papers (for an Application)

☐ Drawing(s)

☐ Licensing-related Papers

☐ Petition Routing Slip (PTO/SB/69) and Accompanying Petition

☐ Petition to Convert to a Provisional Application

☐ Power of Attorney, Revocation Change of Correspondence Address

☐ Terminal Disclaimer

☐ Small Entity Statement

☐ Request for Refund

☐ After Allowance Communication to Group

☐ Appeal Communication to Board of Appeals and Interferences

☐ Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)

☐ Proprietary Information

☐ Status Letter

☐ Additional Enclosure(s) (please identify below):

Remarks

SIGNATURE OF APPLICANT, ATTORNEY OR AGENT

Firm or Individual name

Kenta Suzue, Reg. No. 45,145, WILSON SONSINI GOODRICH & ROSATI

Signature

Date

March 13, 2002

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5-15-02

PATENT
Attorney Docket No.: 24224-702

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
)
Limin He et al.)
)
Application No.: 10/071,862)
)
Filed: February 7, 2002)
)
For: Method And Apparatus For Scalable)
Interconnect Solution)

Group Art Unit: 2825

Examiner: Unknown

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PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Prior to examination of the above-identified patent application on the merits, please amend the application as follows:

IN THE CLAIMS:

Please amend the following claims:

14. (Amended) The method of claim 6, wherein each of the IC design, the first level, and the second level include at least two layers.

15. (Amended) The method of claim 6, wherein each of the IC design, the first level and the second level include one layer.

39. (Amended) The method of claim 36, wherein each of the IC design, the first level, and the second level include at least two layers.

40. (Amended) The method of claim 36, wherein each of the IC design, the first level, and the second level include one layer.